Refine Search

Search Results -
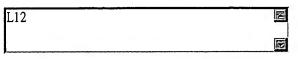
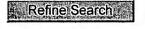| Term	Documents
(7 AND 11).PGPB,USPT.	41
(L11 AND L7).PGPB,USPT.	41

Database:

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US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
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Search: L12

[Refine Search]

[Recall Text] [Clear] [Interrupt]

Search History

DATE: Thursday, May 27, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT; PLUR=YES; OP=OR			
L12	L11 and l7	41	L12
L11	l9 or L10	18461	L11
L10	(712/2-300)[CCLS]	9829	L10
L9	(711/163-221)[CCLS]	9389	L9
L8	(711/163-221)![CCLS]	9389	L8
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L7	L6 and store and load	62	L7
L6	compar$7 near6 (base or partial or offset) near5 address$3 and (load or store) near12 memor$3 near5 external$3	79	L6
L5	L4 and pipelin$7	263	L5
L4	compar$7 near6 (base or partial or offset) near5 address$3 and (load or store) near12 memor$3	760	L4

h   e b   b cg b   e e ch

L3	L1 and external$3 near4 memor$3 and pipelin$7	153	L3
L2	L1 and external$3 near4 memor$3 andpipelin$7	273	L2
L1	compar$7 near6 (base or partial or offset) near5 address$3 and (load or store)	1077	L1

END OF SEARCH HISTORY

h   e b   b cg b   e e ch

WEST

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Search Results - Record(s) 21 through 40 of 54 returned.

☐ 21. Document ID: US 5701448 A

L10: Entry 21 of 54   File: USPT .   Dec 23, 1997

US-PAT-NO: 5701448
DOCUMENT-IDENTIFIER: US 5701448 A

TITLE: Detecting segment limit violations for branch target when the branch unit does not supply the linear address

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 22. Document ID: US 5692168 A

L10: Entry 22 of 54   File: USPT   Nov 25, 1997

US-PAT-NO: 5692168
DOCUMENT-IDENTIFIER: US 5692168 A

TITLE: Prefetch buffer using flow control bit to identify changes of flow within the code stream

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 23. Document ID: US 5511173 A

L10: Entry 23 of 54   File: USPT   Apr 23, 1996

US-PAT-NO: 5511173
DOCUMENT-IDENTIFIER: US 5511173 A

TITLE: Programmable logic array and data processing unit using the same

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 24. Document ID: US 5438670 A

L10: Entry 24 of 54   File: USPT   Aug 1, 1995

US-PAT-NO: 5438670
DOCUMENT-IDENTIFIER: US 5438670 A

TITLE: Method of prechecking the validity of a write access request

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☐   25.   Document ID: US 5408626 A

L10: Entry 25 of 54   File: USPT   Apr 18, 1995

US-PAT-NO: 5408626
DOCUMENT-IDENTIFIER: US 5408626 A

TITLE: One clock address pipelining in segmentation unit

☐   26.   Document ID: US 5263153 A

L10: Entry 26 of 54   File: USPT   Nov 16, 1993

US-PAT-NO: 5263153
DOCUMENT-IDENTIFIER: US 5263153 A

TITLE: Monitoring control flow in a microprocessor

☐   27.   Document ID: US 5226129 A

L10: Entry 27 of 54   File: USPT   Jul 6, 1993

US-PAT-NO: 5226129
DOCUMENT-IDENTIFIER: US 5226129 A

TITLE: Program counter and indirect address calculation system which concurrently
performs updating of a program counter and generation of an effective address

---

☐   28.   Document ID: US 5204953 A

L10: Entry 28 of 54   File: USPT   Apr 20, 1993

US-PAT-NO: 5204953
DOCUMENT-IDENTIFIER: US 5204953 A

TITLE: One clock address pipelining in segmentation unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC

Draw Desc | Image

☐ 29.   Document ID: US 5136696 A

L10: Entry 29 of 54   File: USPT   Aug 4, 1992

US-PAT-NO: 5136696
DOCUMENT-IDENTIFIER: US 5136696 A

TITLE: High-performance pipelined central processor for predicting the occurrence of executing single-cycle instructions and multicycle instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC

Draw Desc | Image

☑ 30.   Document ID: US 5107417 A

L10: Entry 30 of 54   File: USPT   Apr 21, 1992

US-PAT-NO: 5107417
DOCUMENT-IDENTIFIER: US 5107417 A

TITLE: Address translating method for translating virtual address to real address with specified address register to allow bypass of translation steps

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC

Draw Desc | Image

☐ 31.   Document ID: US 5088026 A

L10: Entry 31 of 54   File: USPT   Feb 11, 1992

US-PAT-NO: 5088026
DOCUMENT-IDENTIFIER: US 5088026 A

TITLE: Method for managing a data cache using virtual external storage addresses as arguments

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC

Draw Desc | Image

☐ 32.   Document ID: US 5069654 A

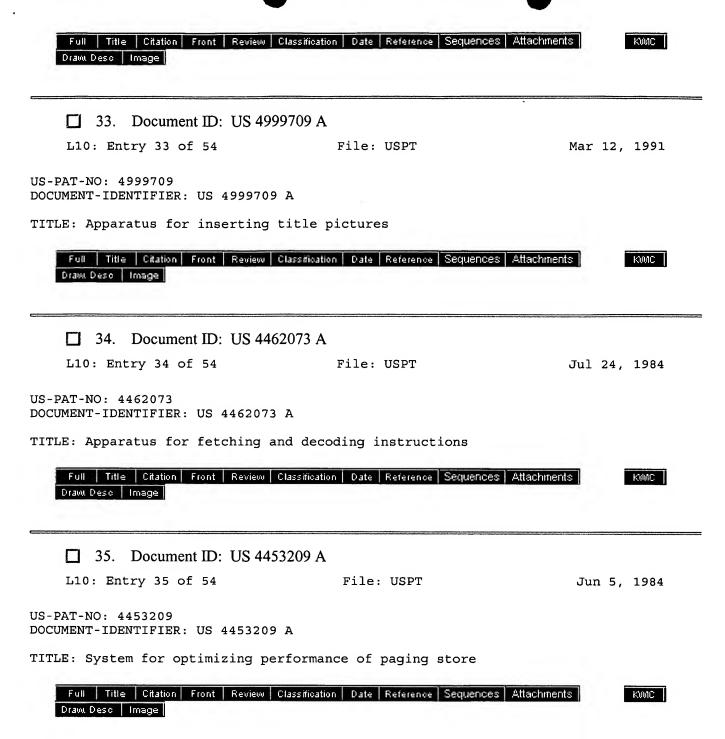L10: Entry 32 of 54   File: USPT   Dec 3, 1991

US-PAT-NO: 5069654
DOCUMENT-IDENTIFIER: US 5069654 A

TITLE: System for drivingly connecting gears, pulleys, wheels, rollers and other apertured components to shafts

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments
Draw Desc | Image

KWIC

☐   33.   Document ID: US 4999709 A

L10: Entry 33 of 54   File: USPT   Mar 12, 1991

US-PAT-NO: 4999709
DOCUMENT-IDENTIFIER: US 4999709 A

TITLE: Apparatus for inserting title pictures

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments
Draw Desc | Image

KWIC

☐   34.   Document ID: US 4462073 A

L10: Entry 34 of 54   File: USPT   Jul 24, 1984

US-PAT-NO: 4462073
DOCUMENT-IDENTIFIER: US 4462073 A

TITLE: Apparatus for fetching and decoding instructions

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments
Draw Desc | Image

KWIC

☐   35.   Document ID: US 4453209 A

L10: Entry 35 of 54   File: USPT   Jun 5, 1984

US-PAT-NO: 4453209
DOCUMENT-IDENTIFIER: US 4453209 A

TITLE: System for optimizing performance of paging store

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments
Draw Desc | Image

KWIC

☐   36.   Document ID: US 4447877 A

L10: Entry 36 of 54   File: USPT   May 8, 1984

US-PAT-NO: 4447877
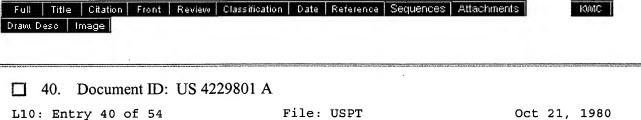DOCUMENT-IDENTIFIER: US 4447877 A

TITLE: Memory bus interface system

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments
Draw Desc | Image

KWIC

☐   37.   Document ID: US 4385352 A

L10: Entry 37 of 54   File: USPT   May 24, 1983

US-PAT-NO: 4385352
DOCUMENT-IDENTIFIER: US 4385352 A

TITLE: Addressing of operands within a segment utilizing segment descriptors

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   38.   Document ID: US 4316244 A

L10: Entry 38 of 54   File: USPT   Feb 16, 1982

US-PAT-NO: 4316244
DOCUMENT-IDENTIFIER: US 4316244 A

TITLE: Memory apparatus for digital computer system

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   39.   Document ID: US 4302809 A

L10: Entry 39 of 54   File: USPT   Nov 24, 1981

US-PAT-NO: 4302809
DOCUMENT-IDENTIFIER: US 4302809 A

TITLE: External data store memory device

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   40.   Document ID: US 4229801 A

L10: Entry 40 of 54   File: USPT   Oct 21, 1980

US-PAT-NO: 4229801
DOCUMENT-IDENTIFIER: US 4229801 A

TITLE: Floating point processor having concurrent exponent/mantissa operation

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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Term	Documents
RELATIVE.USPT.	1220466
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DISTANCES.USPT.	150325
EFFECTIVE.USPT.	926127
EFFECTIVES.USPT.	43
ADDRESS$3	0
ADDRESS.USPT.	200928
ADDRESSA.USPT.	9
(RELATIVE NEAR4 (ADDRESS OR DISTANCE) NEAR9 EFFECTIVE NEAR1 ADDRESS$3).USPT.	54

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Search Results - Record(s) 1 through 20 of 22 returned.

☐ 1. Document ID: US 6338128 B1

L13: Entry 1 of 22   File: USPT   Jan 8, 2002

US-PAT-NO: 6338128
DOCUMENT-IDENTIFIER: US 6338128 B1

TITLE: System and method for invalidating an entry in a translation unit

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 2. Document ID: US 6336160 B1

L13: Entry 2 of 22   File: USPT   Jan 1, 2002

US-PAT-NO: 6336160
DOCUMENT-IDENTIFIER: US 6336160 B1

TITLE: Method and system for dividing a computer processor register into sectors and storing frequently used values therein

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 3. Document ID: US 6226715 B1

L13: Entry 3 of 22   File: USPT   May 1, 2001

US-PAT-NO: 6226715
DOCUMENT-IDENTIFIER: US 6226715 B1

TITLE: Data processing circuit with cache memory and cache management unit for arranging selected storage location in the cache memory for reuse dependent on a position of particular address relative to current address

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 4. Document ID: US 6157981 A

L13: Entry 4 of 22   File: USPT   Dec 5, 2000

US-PAT-NO: 6157981

DOCUMENT-IDENTIFIER: US 6157981 A

TITLE: Real time invariant behavior cache

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐ 5.   Document ID: US 5956520 A

L13: Entry 5 of 22   File: USPT   Sep 21, 1999

US-PAT-NO: 5956520
DOCUMENT-IDENTIFIER: US 5956520 A

TITLE: Microcomputer for accessing a memory outside the chip by using an address
generated from the CPU

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐ 6.   Document ID: US 5771047 A

L13: Entry 6 of 22   File: USPT   Jun 23, 1998

US-PAT-NO: 5771047
DOCUMENT-IDENTIFIER: US 5771047 A

TITLE: Graphics computer

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐ 7.   Document ID: US 5647698 A

L13: Entry 7 of 22   File: USPT   Jul 15, 1997

US-PAT-NO: 5647698
DOCUMENT-IDENTIFIER: US 5647698 A

TITLE: Tire rasp blade

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐ 8.   Document ID: US 5446876 A

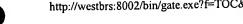L13: Entry 8 of 22   File: USPT   Aug 29, 1995
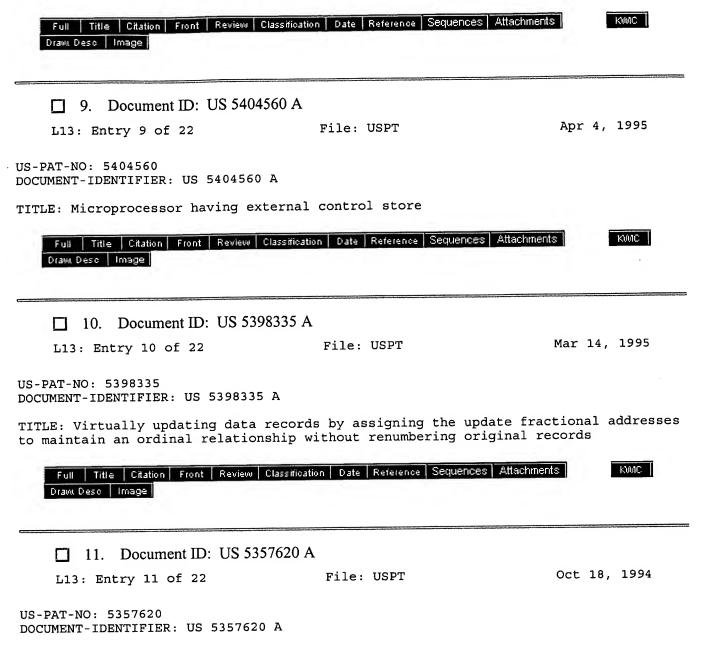
US-PAT-NO: 5446876
DOCUMENT-IDENTIFIER: US 5446876 A

TITLE: Hardware mechanism for instruction/data address tracing

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments   KWC
Draw Desc | Image

☐ 9. Document ID: US 5404560 A

L13: Entry 9 of 22   File: USPT   Apr 4, 1995

US-PAT-NO: 5404560
DOCUMENT-IDENTIFIER: US 5404560 A

TITLE: Microprocessor having external control store

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments   KMC
Draw Desc | Image

☐ 10. Document ID: US 5398335 A

L13: Entry 10 of 22   File: USPT   Mar 14, 1995

US-PAT-NO: 5398335
DOCUMENT-IDENTIFIER: US 5398335 A

TITLE: Virtually updating data records by assigning the update fractional addresses
to maintain an ordinal relationship without renumbering original records

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments   KWC
Draw Desc | Image

☐ 11. Document ID: US 5357620 A

L13: Entry 11 of 22   File: USPT   Oct 18, 1994

US-PAT-NO: 5357620
DOCUMENT-IDENTIFIER: US 5357620 A

TITLE: Bit addressing system

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments   KWC
Draw Desc | Image

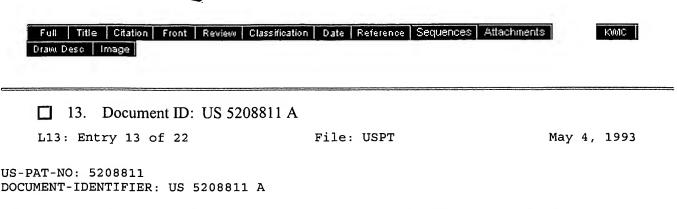☐ 12. Document ID: US 5283935 A

L13: Entry 12 of 22   File: USPT   Feb 8, 1994

US-PAT-NO: 5283935
DOCUMENT-IDENTIFIER: US 5283935 A

TITLE: Tire rasp blade

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image | | | | | | | | | | |

☐   13.   Document ID: US 5208811 A

L13: Entry 13 of 22   File: USPT   May 4, 1993

US-PAT-NO: 5208811
DOCUMENT-IDENTIFIER: US 5208811 A

TITLE: Interconnection system and method for heterogeneous networks

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image | | | | | | | | | | |

☐   14.   Document ID: US 5168560 A

L13: Entry 14 of 22   File: USPT   Dec 1, 1992

US-PAT-NO: 5168560
DOCUMENT-IDENTIFIER: US 5168560 A

TITLE: Microprocessor system private split cache tag stores with the system tag store having a different validity bit for the same data line

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image | | | | | | | | | | |

☑   15.   Document ID: US 5095424 A
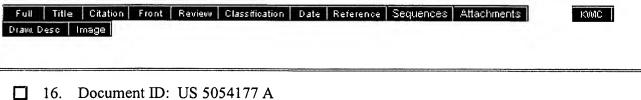
L13: Entry 15 of 22   File: USPT   Mar 10, 1992

US-PAT-NO: 5095424
DOCUMENT-IDENTIFIER: US 5095424 A

TITLE: Computer system architecture implementing split instruction and operand cache line-pair-state management

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image | | | | | | | | | | |

☐   16.   Document ID: US 5054177 A

L13: Entry 16 of 22   File: USPT   Oct 8, 1991

US-PAT-NO: 5054177
DOCUMENT-IDENTIFIER: US 5054177 A

TITLE: Tire rasp blade

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐　17.　Document ID: US 4885847 A

L13: Entry 17 of 22　　　　　　File: USPT　　　　　　Dec 12, 1989

US-PAT-NO: 4885847
DOCUMENT-IDENTIFIER: US 4885847 A

TITLE: Golf club measuring and fitting apparatus

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐　18.　Document ID: US 4866445 A

L13: Entry 18 of 22　　　　　　File: USPT　　　　　　Sep 12, 1989

US-PAT-NO: 4866445
DOCUMENT-IDENTIFIER: US 4866445 A

TITLE: Method and programmable device for transcoding character strings

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐　19.　Document ID: US 4396940 A

L13: Entry 19 of 22　　　　　　File: USPT　　　　　　Aug 2, 1983

US-PAT-NO: 4396940
DOCUMENT-IDENTIFIER: US 4396940 A

TITLE: Color original evaluating method

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐　20.　Document ID: US 4009415 A

L13: Entry 20 of 22　　　　　　File: USPT　　　　　　Feb 22, 1977

US-PAT-NO: 4009415
DOCUMENT-IDENTIFIER: US 4009415 A

TITLE: Plasma panel with dynamic keep-alive operation utilizing a lagging sustain
signal

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

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Term	Documents
DISTANCE.USPT.	859783
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